



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Gelinas *et al.*

Appl. No.: 09/637,500

Filed: August 11, 2000

For: **High Performance RISC  
Instruction Set Digital Signal  
Processor Having Circular Buffer  
and Looping Controls (as amended)**

Confirmation No.: 7608

Art Unit: 2181

Examiner: Tsai, Henry

Atty. Docket: 1778.2090001

(MIPS 0142.00US)

**Amendment and Reply Under 37 C.F.R. § 1.111**

***Mail Stop Amendment***

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated December 8, 2005, (PTO Prosecution File Wrapper Paper No. 1206), Applicants submit the following Amendment and Remarks.

It is not believed that extensions of time or fees for net addition of claims are required beyond those that may otherwise be provided for in documents accompanying this paper. However, if additional extensions of time are necessary to prevent abandonment of this application, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required therefor (including fees for net addition of claims) are hereby authorized to be charged to our Deposit Account No.

19-0036.

***Amendment to the Title***

Please change the title to:

"High Performance RISC Instruction Set Digital Signal Processor Having  
Circular Buffer ~~Control~~ and Looping ~~Instruction Commands~~ Controls"